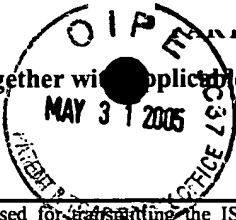


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Patrick G. Burns, Esq.  
GREER, BURNS & CRAIN, LTD.  
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06/01/2005 YPOLITE2 00000057 10808833

01 FC:1501 1400.00 OP  
02 FC:1504 300.00 OP

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/808,833	03/25/2004	Kazushige Hotta	2803.70178	6705

**TITLE OF INVENTION:** THIN FILM TRANSISTOR SUBSTRATE AND MANUFACTURING METHOD THEREOF

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	07/13/2005

EXAMINER	ART UNIT	CLASS-SUBCLASS
QUACH, TUAN N	2826	257-057000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.563).

- ☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
- ☐ "Fee Address" indication (or "Fee Address" Indication form PTO/SB/47; Rev 03-02 or more recent) attached. Use of a Customer Number is required.

2. For printing on the patent front page, list

- (1) the names of up to 3 registered patent attorneys or agents OR, alternatively,
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3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

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(B) RESIDENCE: (CITY and STATE OR COUNTRY)

FUJITSU DISPLAY

TECHNOLOGIES CORPORATION

Kawasaki, Japan

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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5. Change in Entity Status (from status indicated above)

- ☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.
- ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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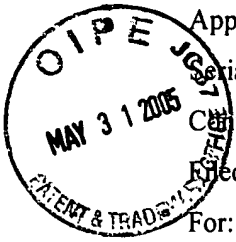
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Kazushige Hotta  
Serial No.: 10/808,833  
Conf. No.: 6705  
Filed: 3/25/2004  
For: THIN FILM TRANSISTOR  
SUBSTRATE AND  
MANUFACTURING METHOD  
THEREOF  
Art Unit: 2826  
Examiner: Quach, Tuan N.

*I hereby certify that this paper is being deposited with the United States Postal Service as FIRST-CLASS mail in an envelope addressed to: MS Issue Fee, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this date.*

26 May 05

Date

Registration No. 29,367  
Attorney for Applicant(s)

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Respectfully submitted,

GREER, BURNS & CRAIN, LTD.

By 

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May 26, 2005

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